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Client Reference: OSP-15115



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of:
TAKAAKI SHIOTA ET AL.

Confirmation Number: 5650

Application No.: 10/706,266

Group Art Unit:

Filed: November 13, 2003

Examiner:

Title: SILICON WAFER AND MANUFACTURING METHOD THEREOF

**Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

TRANSMITTAL OF FORMAL DRAWINGS

Attached are the formal drawings for this application. 4 Sheets of Figures 1-5B.

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